

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Application Type:: REGULAR  
Subject Matter:: UTILITY  
CD-ROM or CD-R?:: NONE  
Title:: SEMICONDUCTOR INTEGRATED  
CIRCUIT, SYSTEM BOARD AND  
DEBUGGING SYSTEM  
Attorney Docket Number:: 217806US2  
Total Drawing Sheets:: 13

### INVENTOR INFORMATION

Applicant Authority Type:: INVENTOR  
Primary Citizenship Country:: Japan  
Status:: FULL CAPACITY  
Given Name:: Takashi  
Family Name:: MIURA  
Street of Mailing Address:: c/o Intellectual Property Division  
Toshiba Corporation  
1-1, Shibaura 1-chome, Minato-ku  
City of Mailing Address:: Tokyo  
Country of Mailing Address:: Japan

Applicant Authority Type:: INVENTOR  
Primary Citizenship Country:: Japan  
Status:: FULL CAPACITY  
Given Name:: Takashi  
Family Name:: MIYAMORI  
Street of Mailing Address:: c/o Intellectual Property Division  
Toshiba Corporation  
1-1, Shibaura 1-chome, Minato-ku  
City of Mailing Address:: Tokyo  
Country of Mailing Address:: Japan

### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

### REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

# FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2000-402630	Japan	12/28/00	YES

## ASSIGNMENT INFORMATION

Assignee Name::

KABUSHIKI KAISHA TOSHIBA

Street of Mailing Address::

1-1, Shibaura 1-chome,  
Minato-ku

City of Mailing Address::

Tokyo

Country of Mailing Address::

Japan